

## AB-X32APXXXXX-X Series

Rev. F

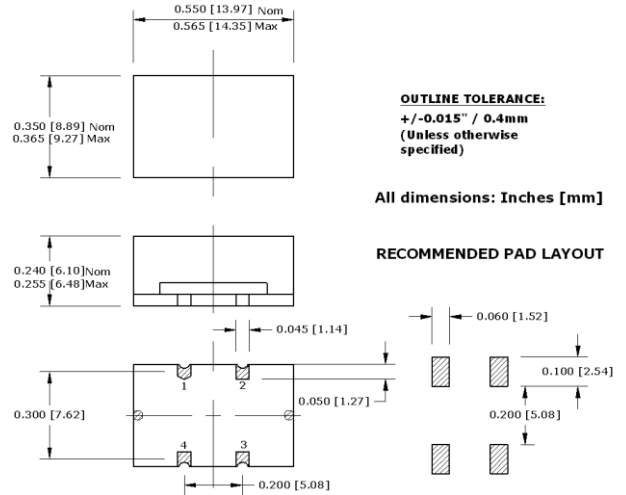
### SMD VCXO

### Ultra-Low Phase Noise with Low G Sensitivity

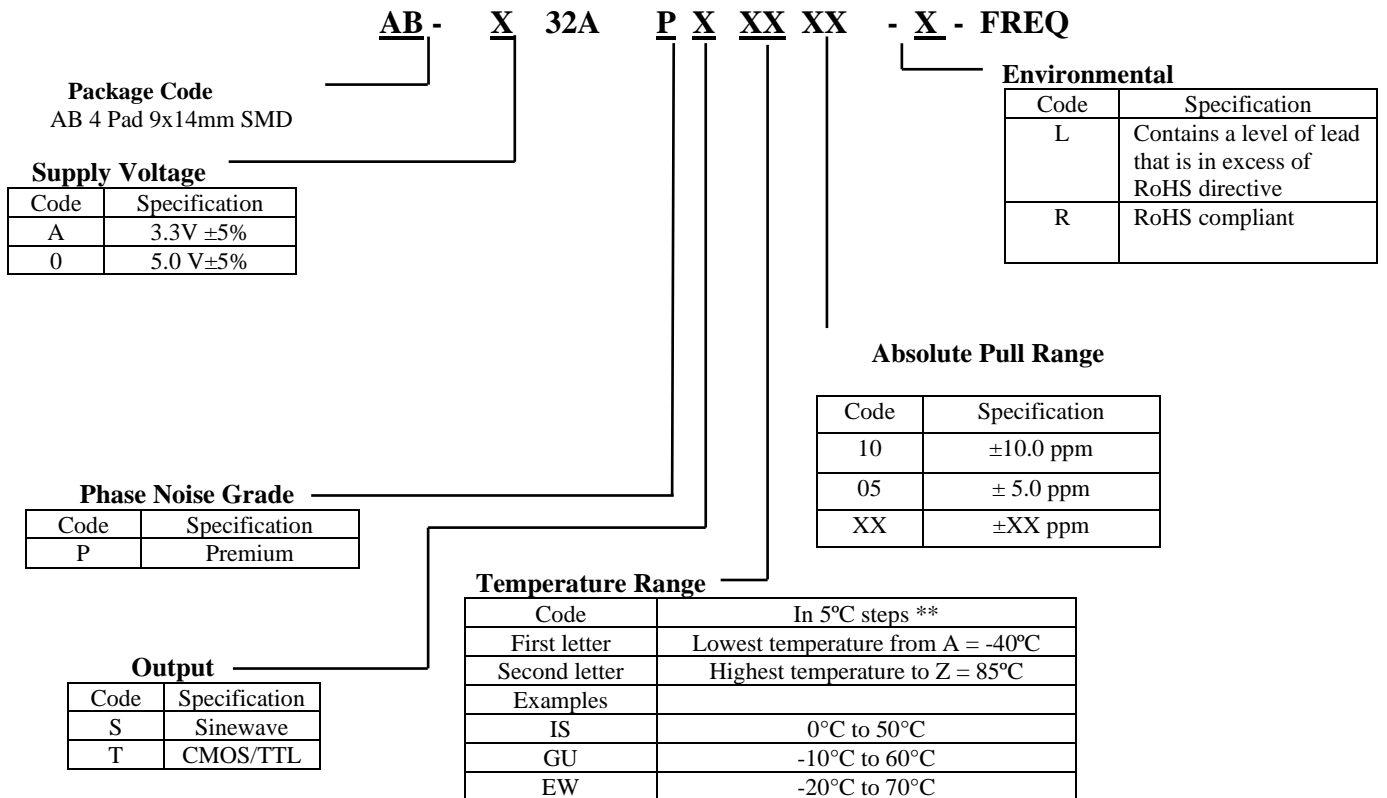
**Description:** The AB-X32APXXXXX-X Series of SMD Voltage Controlled crystal oscillators (VCXO), provides extremely low phase noise and jitter with Sine-wave or CMOS output in a small surface mount FR4 based package.

## Features

- Small, Low Profile SMD Package
- Ultra-Low Phase Noise
- Low G sensitivity
- No Multiplication – no sub-harmonics
- COTS/Dual use



## Creating a Part Number

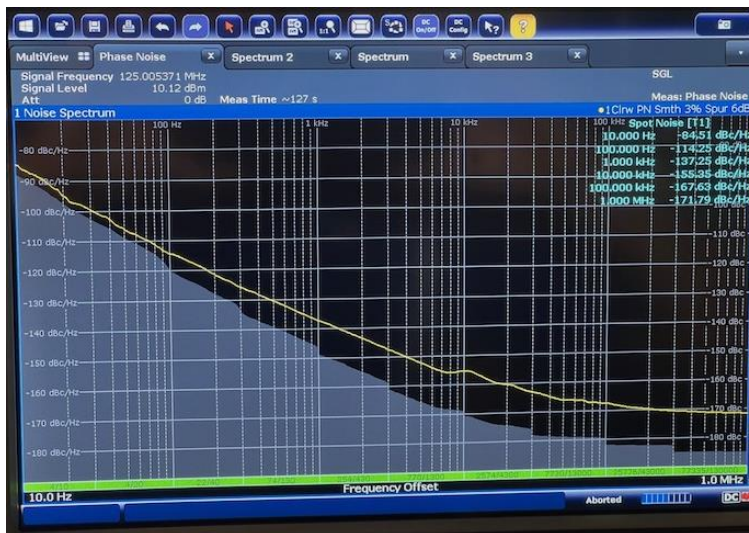


**\*\*Temperature Code Table**

Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C	Letter	Temp °C
A	-40	F	-15	K	10	P	35	U	60	Z	85
B	-35	G	-10	L	15	Q	40	V	65		
C	-30	H	-5	M	20	R	45	W	70		
D	-25	I	0	N	25	S	50	X	75		
E	-20	J	5	O	30	T	55	Y	80		

\*Note: Not all combinations are available

125 MHz example



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Note 1) All parameters, unless otherwise specified, are at nominal conditions, i.e.: T=25°C, Nominal Vcc & Nominal Load

### Specifications

Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
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<i>Electrical</i>							
Frequency Range	F		80		125	MHz	
Input Voltage	Vcc		3.135 4.75	3.30 5.00	3.465 5.25	V	A 0
Input Current	Icc	Sine			30	mA	
Frequency Stability	ΔF/F	vs. Temperature vs. Vcc aging		±10 ±0.1 ±1 ±3 ±4		ppm ppm/V ppm/year ppm ppm	See chart  First Year 7 years 10 years
G-sensitivity		Worst Direction		0.2		ppb/G	
Setability	Vset		Vcc/2 - 0.1Vcc	Vcc/2	Vcc/2 + 0.1 Vcc	V	
Load		Sine CMOS	Internally AC-coupled 50 Ohm 10 KOhm // 15 pF				
Output power (output code "S")	P	Sine-wave Into 50 Ohms	7	10		dBm	
Logic 1 (CMOS)	Voh		0.9Vcc			V	Output Code T
Logic 0 (CMOS)	Vol				0.1	V	Output Code T
Duty Cycle			45/55		55/45	%	Output Code T
Rise/Fall Time	Tr/Tf			2	3		Output Code T
Spurious		Not setup related			-80	dBc	
Harmonics		Sine-wave		-30	-25	dBc	
SSB Phase Noise	£(Δf)	@ 10 Hz @ 100 Hz @ 1 KHz @ 10 KHz @ 100 KHz		-90 -120 -146 -160 -168		dBc/Hz	@ 100MHz, Grade P, APR ±25
		@ 10 Hz @ 100 Hz @ 1 KHz @ 10 KHz @ 100 KHz		-80 -110 -135 -155 -168			@ 125 MHz, Grade P, APR ±20
Input Impedance			>10K Ohm				
Control voltage	Vc		0.15 0		3.15 5.0	V	Code A Code 0
Modulation bandwidth	MB		DC		10	KHz	*
Initial Pullability	ΔF/F	As shipped		±50		ppb	Depends on APR
Absolute Pull Range (Guaranteed Capture Range)	ΔF/F	Over All Conditions, Specify in the part number			±25	ppm	Includes temperature, Vcc variations, and aging 10 years

### Absolute Maximum Ratings

Input Break Down Voltage	Vcc		-0.5		5.5	V	
Storage temper.	Ts		-55		85	°C	
Control Voltage	Vc		-1		5.5	V	

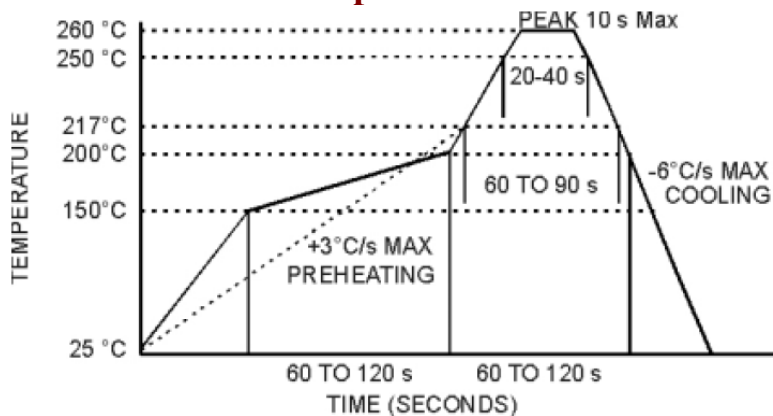
### Environmental and Mechanical

<b>Operating temp. range</b>	-40°C to 85°C MAX
<b>Mechanical Shock</b>	Per MIL-STD-202, Method 213, Cond. E
<b>Thermal Shock</b>	Per MIL-STD-883, Method 1011, Cond. A
<b>Vibration</b>	Per MIL-STD-883, Method 2007, Cond. A
<b>Soldering Conditions</b>	See MAX reflow profile; The device may be reflowed once. Reflowing upside down is not allowed. NO CLEAN assembly is recommended.
<b>Hermetic Seal</b>	Leak rate less than $1 \times 10^{-8}$ atm.cc/s of helium (crystal only)

### Electrical Connections

Pin out	Pin 1=Vc; Pin 2= GND; Pin 3=Output; Pin 4= Vcc
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### Maximum solder reflow profile



The device may be reflowed once. Reflowing upside down is not allowed. NO CLEAN assembly is recommended.

\*The MBW can be extended to 20 KHZ by special requirements on the best effort basis.